# IES COLLEGE OF TECHNOLOGY, BHOPAL

B.E. (6$^{th}$ SEM) Assignment -1

Microprocessor and microcontroller (CS -601)

**Date of issued:  13/01/14**                          **Date of Submission: 27/01/14**

| Q.1 | Explain the silent features of 8056 family of microprocessor. | |
|-----|---------------------------------------------------------------|-|
| Q.2 | What is latch up and buffer? | |
| Q.3 | Explain the following terms: a) main memory b) magnetic memory c) cache memory | |
| Q.4 | What do you mean by pipeline architecture | |
| Q.5 | Draw the pin diagram of 8086. Explain its minimum mode. | |

# IES COLLEGE OF TECHNOLOGY, BHOPAL

B.E. (6$^{th}$ SEM) Assignment -1

PPL (CS 602)

**Date of issued:  08 /01/14**                          **Date of Submission: 27/01/14**

| Q.1 | Give the difference between Imperative, Objected oriented, Functional Programming |
|-----|-----------------------------------------------------------------------------------|
| Q.2 | Show the parse tree for the string : result=number number*number |
| Q.3 | Give the difference between syntax and  Semantics |

# IES COLLEGE OF TECHNOLOGY, BHOPAL

B.E. (6$^{th}$ SEM) Assignment -1

(CS 603)

**Date of issued: 10/01/14**                          **Date of Submission: 27/01/14**

| Q.1 | What is difference between RAD and RUP Model? | |
|-----|-----------------------------------------------|-|
| Q.2 | Differentiate between CBD and spiral model? | |
| Q.3 | What is CMM and Agile process? | |
| Q.4 | What is prototyping model? | |
| Q.5 | What is software process customization? | |

# IES COLLEGE OF TECHNOLOGY, BHOPAL
## B.E. (6<sup>th</sup> SEM) Assignment -1
## COMPUTER NETWORK (CS 604)

**Date of issued:  06 /01/14**                **Date of Submission: 27/01/14**

| | | |
|---|---|---|
| Q.1 | Define 3 Layers Of X.25. | |
| Q.2 | Explain What Is ATM. | |
| Q.3 | What Is Internet, Intranet And Entranet | |
| Q.4 | Explain Sna Dna with their example. | |
| Q.5 | Explain Various N/W Topologies. | |